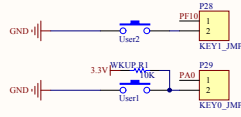
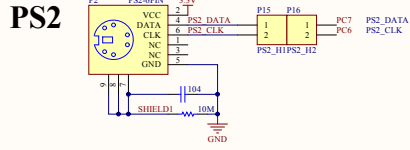
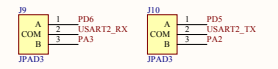
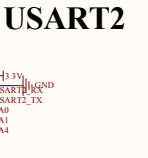
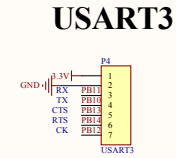


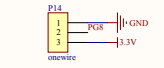
SPI2 SPI1



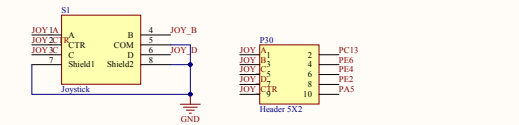
Key



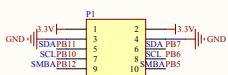
PS2



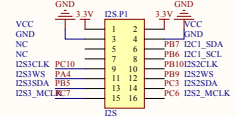
ONEWIRE



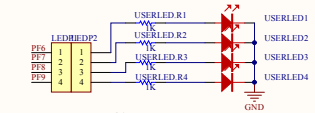
Joystick



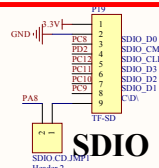
I2C2 I2C1



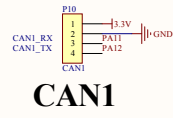
I2S3 I2S2



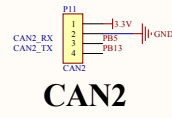
USER LED



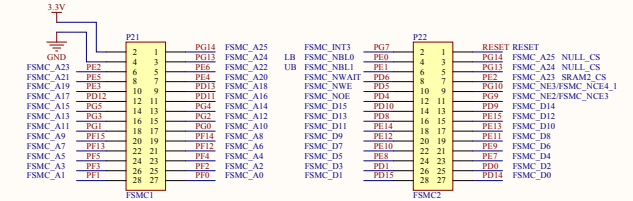
SDIO



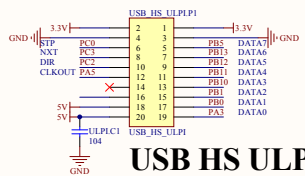
CAN1



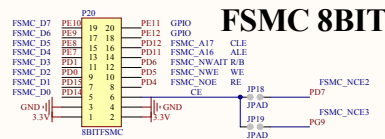
CAN2



FSMC SRAM NOR

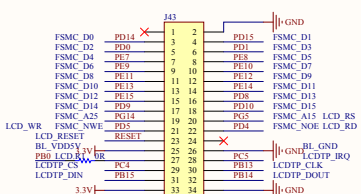


USB HS ULPI

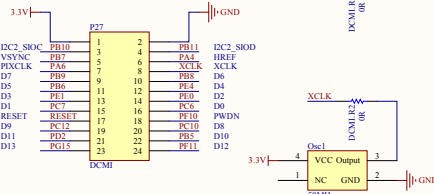


FSMC 8BIT

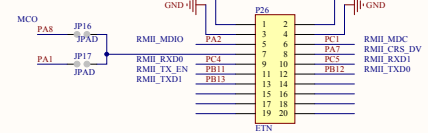
FSMC LCD



DCMI

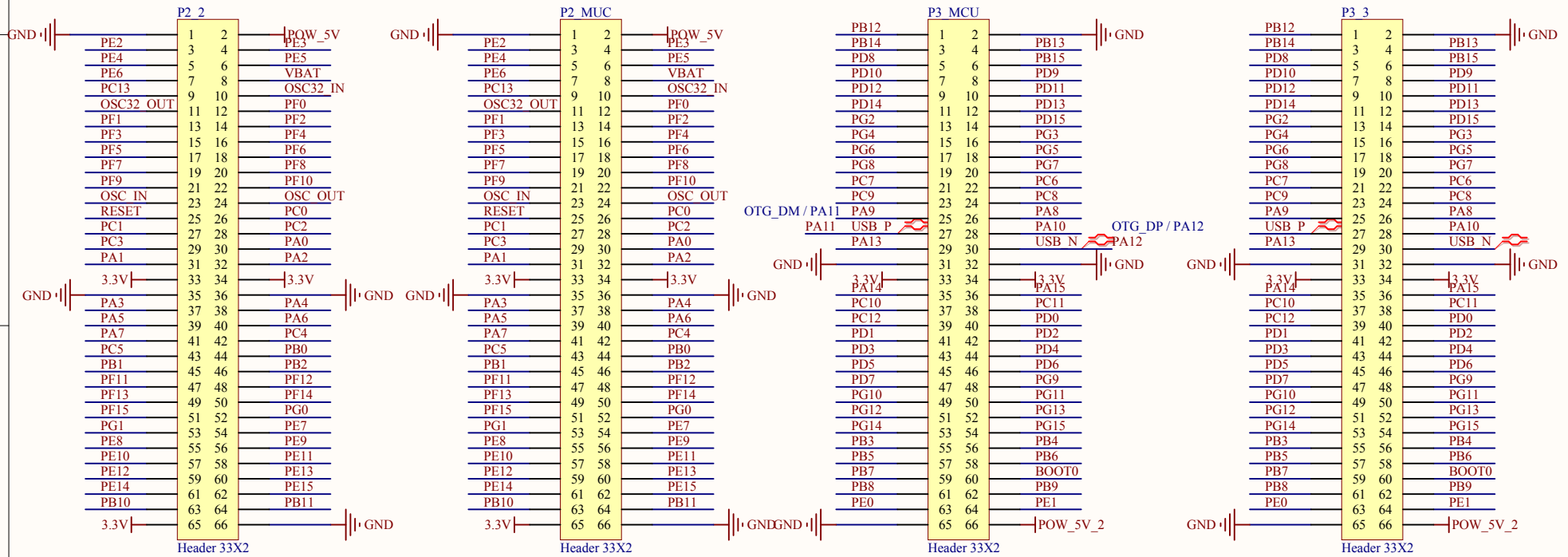


ETH



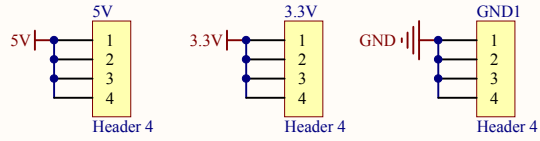
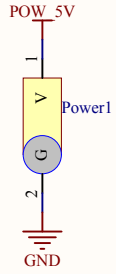
Title			
Size	Number	Revision	
A2	2013/06/15	Sheet of	
File	Funcb_Interface_SchDas	Drawn By	

Pin Port

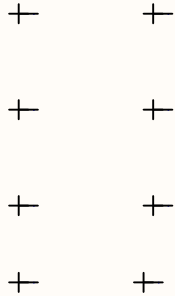
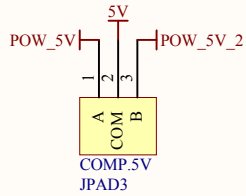


ALL GPIO

Title		
Size	Number	Revision
A4		
Date:	2013/6/15	Sheet of
File:	E:\pcb\...MCU.SchDoc	Drawn By:



POWER



Title		
Size A4	Number	Revision
Date:	2013/6/15	Sheet of
File:	E:\pcb\...\Power.SchDoc	Drawn By: